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Sunghyun Lee, Sungjoo Yoo, Kiyoung Choi

May 2002 **Proceedings of the tenth international symposium on Hardware/software codesign**

Publisher: ACM Press

Full text available: pdf(658.77 KB) Additional Information: full citation, abstract, references, index terms

We present a method of runtime configuration scheduling in reconfigurable SoC design. As a model of computation in system representation, we use a popular formal model of computation, hierarchical FSM (HFSM) with synchronous dataflow (SDF) model, in short HFSM-SDF model. In reconfigurable SoC design with the HFSM-SDF model, the problem of configuration scheduling is challenging due to the dynamic behavior of the system such as concurrent execution of state transitions (by AND relation), complex ...

2 Power modeling and optimization for embedded systems: Requirement-based design methods for adaptive communications links

Juan Antonio Carballo, Kevin Nowka, Seung-Moon Yoo, Ivan Vo, Clay Cranford, Robert Norman

June 2004 **Proceedings of the 41st annual conference on Design automation**

Publisher: ACM Press

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High-speed communications link cores must consume low-power, feature low bit-error-rates (BER), and address many applications. We present a methodology to design adaptive link architectures, whereby the link's internal logic complexity, frequency, and supply are simultaneously adapted to application requirements. The requirement space is mapped to the design space using requirements measurement circuits and configurable logic blocks. CMOS results indicate that power savings of 60 versus the wors ...

Keywords: communication architectures, energy efficient design

3 Bit-split string-matching engines for intrusion detection and prevention

Lin Tan, Brett Brotherton, Timothy Sherwood

March 2006 **ACM Transactions on Architecture and Code Optimization (TACO)**, Volume 3 Issue 1

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